STUDY MODULE DESCRIPTION FORM

Name of the module/subject **Programmable circuits**	Code **1010331411010334197**

Field of study **Information Engineering**	Profile of study (general academic, practical) **general academic**	Year /Semester **1 / 1**

Elective path/specialty **-**	Subject offered in: **polish**	Course (compulsory, elective) **obligatory**

Cycle of study: **First-cycle studies**	Form of study (full-time,part-time) **full-time**

No. of hours Lecture: **2** Classes: **-** Laboratory: **2** Project/seminars: **-**	No. of credits **6**

Status of the course in the study program (Basic, major, other) **other**	(university-wide, from another field) **university-wide**

Education areas and fields of science and art **technical sciences**	ECTS distribution (number and %**)** **6 100%**

Responsible for subject / lecturer:

dr inż. Krzysztof Chmiel
email: krzysztof.chmiel@put.poznan.pl
tel. 61 665 35 31
Wydział Elektryczny
ul. Piotrowo 3A 60-965 Poznań

Prerequisites in terms of knowledge, skills and social competencies:

1	**Knowledge**	K_W00: has basic knowledge resulting from the secondary school program.
		K_W01: has basic knowledge in the field of mathematics, containing algebra, analysis, logic, probability theory, as well as elements of discrete and applied mathematics.
2	**Skills**	K_U01:is able to gain (inquire) information from literature, data bases and other sources; is able to integrate acquired information, interpret it, as well as to draw conclusions and also formulate and defend opinions.
		K_U06: is able to communicate in English, and also to read descriptions and instructions concerning electronic devices, computer hardware and software tools, and similar documents.
3	**Social competencies**	K_K00: has social competences resulting from the secondary school program.
		K_K04: is aware of responsibility for individual work, and also is prepared to respect the rules of collective work, and to bear responsibility for collective projects.

Assumptions and objectives of the course:

Knowledge of mathematical models, methods of synthesis and CAD tools of digital circuits.

Study outcomes and reference to the educational results for a field of study

Knowledge:

1. Has systematized and improved theoretically knowledge in the domain of analog and digital electronic devices, and also programmable circuits. - [K_W03]

Skills:

1. Can prepare technical report concerning the realization of the engineering task, and also is able to prepare a text containing the discussion of the results. - [K_U03]

2. Is able to construct, to activate and to test simple electronic devices and programmable circuits, as well as, in the case of error detection, to diagnose them. - [K-U08]

Social competencies:

1. Is aware of responsibility for individual work, and also is prepared to respect the rules of collective work, and to bear responsibility for collective projects. - [K_K04]

2. Is aware of importance: of the project realization precision, of notational standards, of language correctness, and of task punctuality. - [K_K07]

Assessment methods of study outcomes

Credit for lectures and laboratory exercises.

Course description

Lectures: Combinatorial and sequential digital circuits. Boolean functions and finite automata as mathematical models of the circuits. Realization of Boolean functions with use of logic gates, multiplexors, demultiplexors, ROMs and logic arrays. Realization of automata with use of flip-flops. Integrated digital circuits. Microprogrammed circuits and flow diagrams. Concurrent circuits and Petri nets. CAD tools.

Laboratory program: Analysis of combinatorial circuits (UK). Synthesis of combinatoral circuits. Realization of UK with use of NAND gates. Realization of UK with use of multiplexors. Realization of UK with use of demultiplexors. Realization of UK with use of ROMs. Analysis of sequential circuits (US). Realization of US with use of D-NAND structure. Realization of US with use of JK-NAND structure. Realization of US with use of memory-register structure. Realization of asynchronous US. Realization of microprogrammed circuits ? control circuit. Realization of microprogrammed circuits ? operational circuit. Realization of concurrent circuits. Conclusion.

Basic bibliography:

1. Teoria układów logicznych, K. Chmiel, Wydawnictwo Politechniki Poznańskiej, Poznań, 1994, 1995

2. Układy cyfrowe - wykłady, K. Chmiel, PowerPoint, Poznań, 2004

Additional bibliography:

1. Układy scalone TTL w systemach cyfrowych, J. Pieńkos, J. Turczyński, WNT, Warszawa, 1980

2. Podstawy projektowania układów cyfrowych, C. Zieliński, PWN, Warszawa, 2003

Result of average student's workload

Activity	Time (working hours)
1. Lectures.	30
2. Laboratory exercises.	30
3. Consultations and examination.	15
4. Preparation to laboratory exercises and elaboration of reports.	45
5. Preparation to tests and examination.	30

Student's workload

Source of workload	hours	ECTS
Total workload	150	6
Contact hours	75	3
Practical activities	75	3